Appln No. 10/821,505 Inventors: Steve C. Huang et al. Atty. Docket No.: ID-04-01 Page 1 of 13 First Class U.S. Mail Filed: April 9, 2004
Title: METHOD AND SYSTEM FOR PROVIDING FAST DESIGN FOR
TESTABILITY PROTOTYPING IN INTEGRATED CIRCUIT DESIGNS
Attorney: Kuni Oh, Esq.
Jackson & Co., LLP

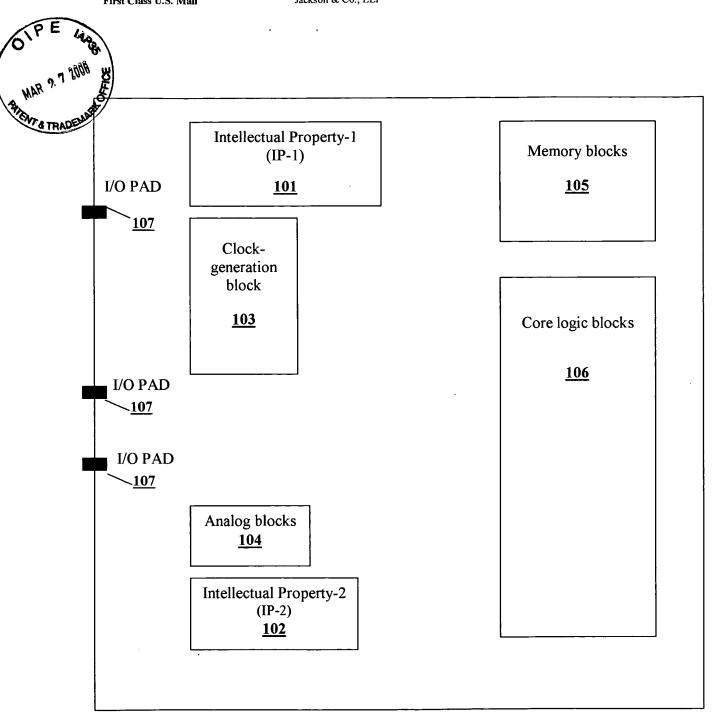


FIGURE 1

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Attorney: Kuni Oh, Esq. Jackson & Co., LLP

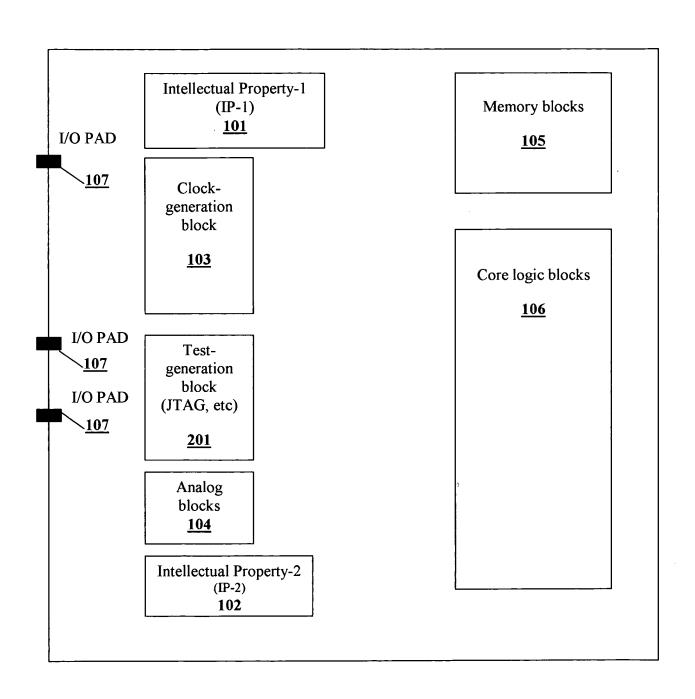


FIGURE 2

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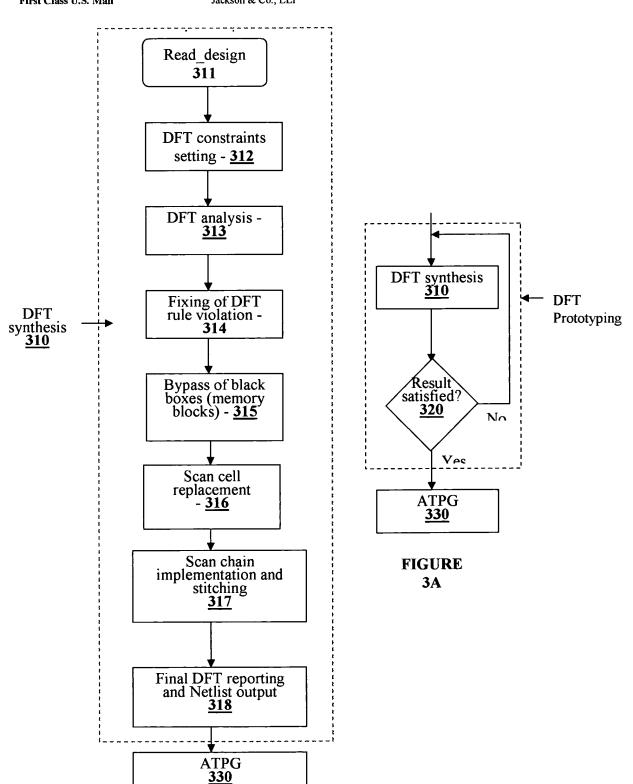
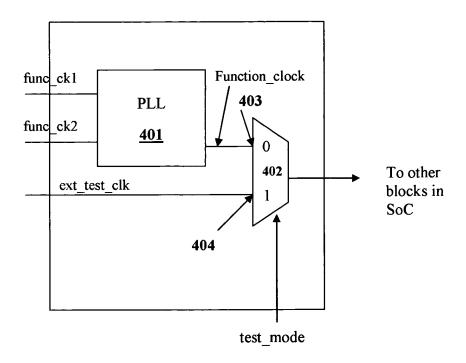


FIGURE 3B

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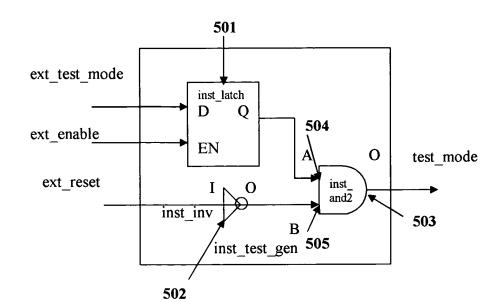
<u>400</u>

Figure 4

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Jackson & Co., LLP

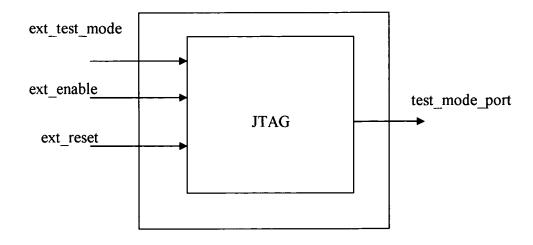


<u>500</u>

Figure 5

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Jackson & Co., LLP



<u>600</u>

FIGURE 6

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Attorney: Kuni Oh, Esq. Jackson & Co., LLP

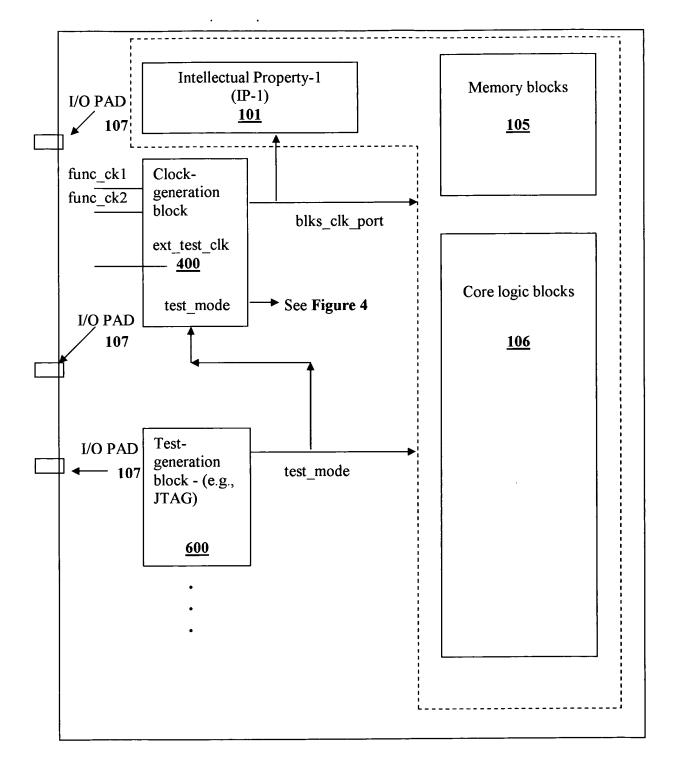


FIGURE 7

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Attorney: Kuni Oh, Esq.

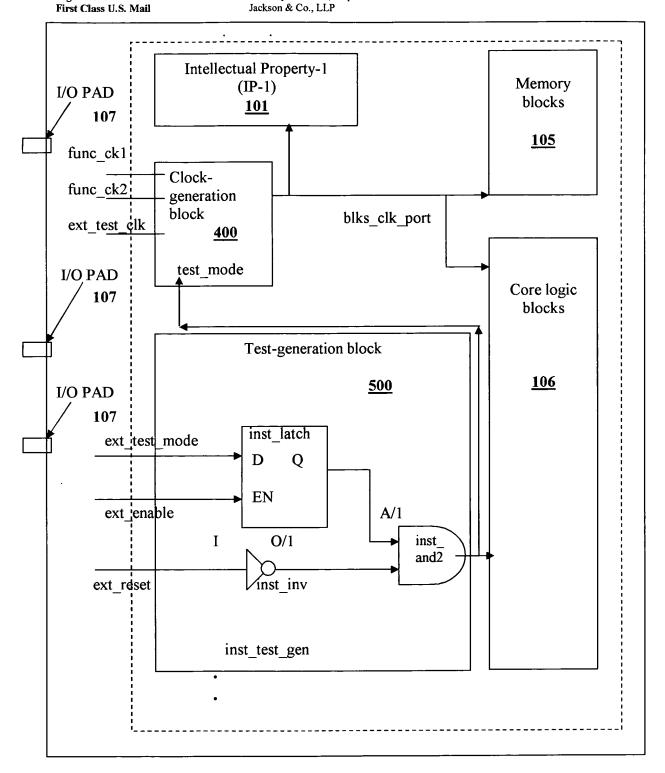


FIGURE 8

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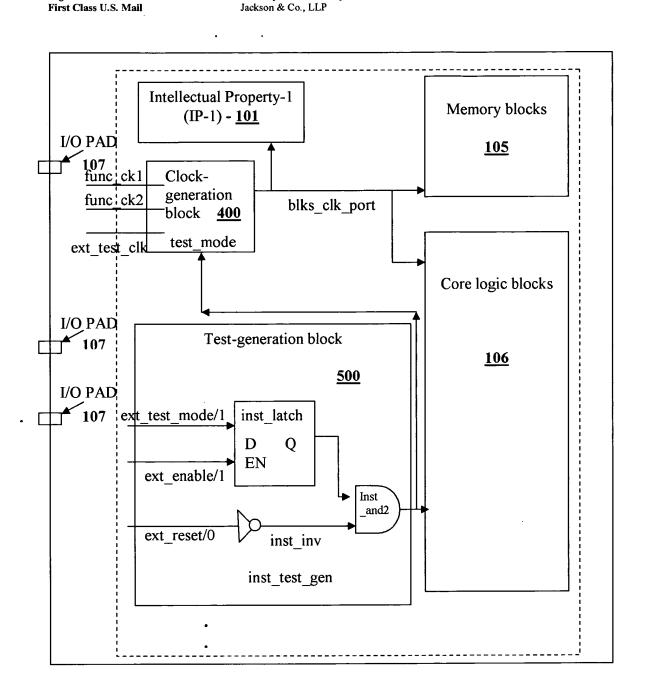
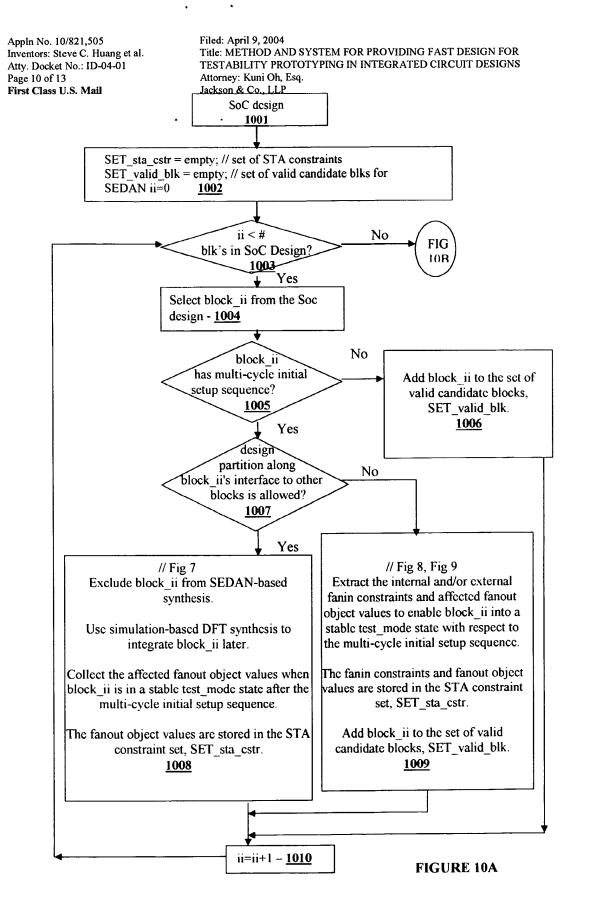
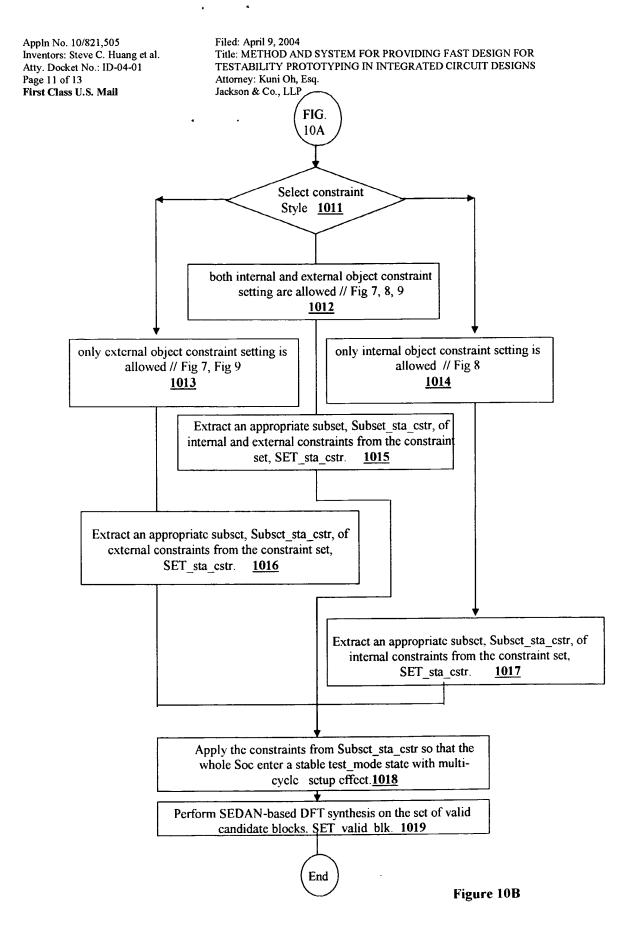


FIGURE 9





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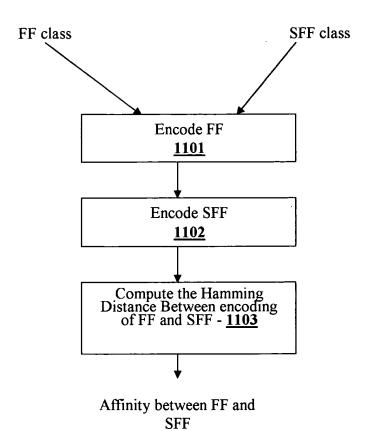


Figure 11

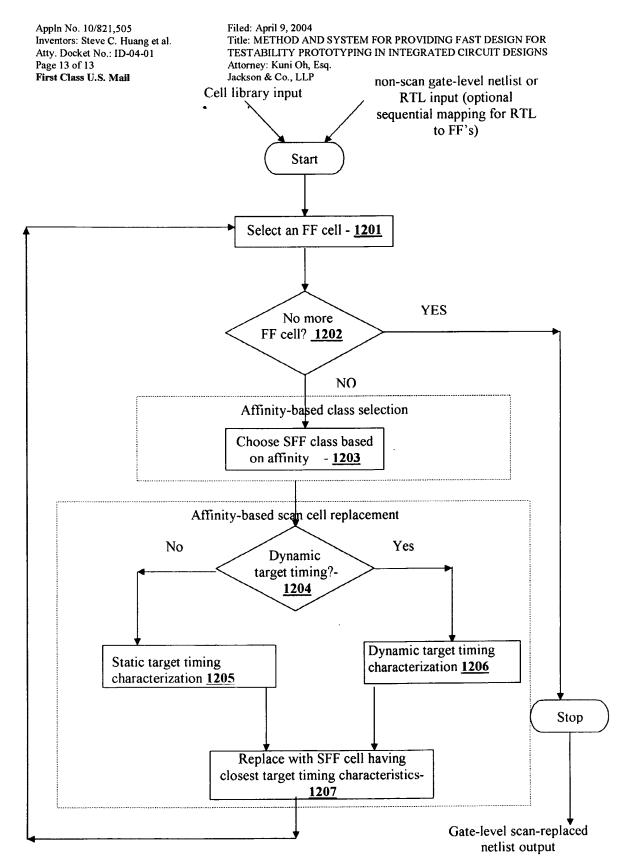


FIGURE 12